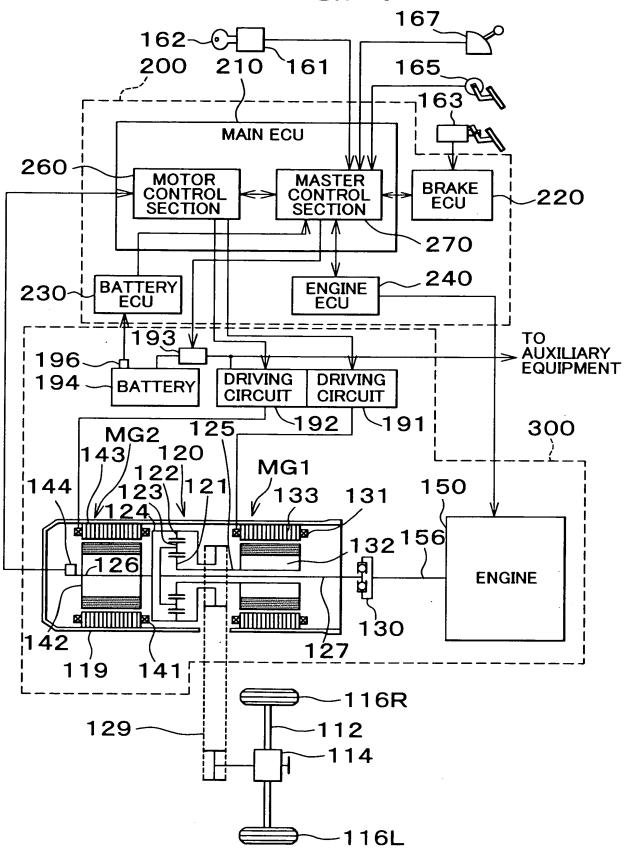
OBLON, SPIVAK, ET AL DOCKET #: 205007US2 INV: Mitsuhiro NADA

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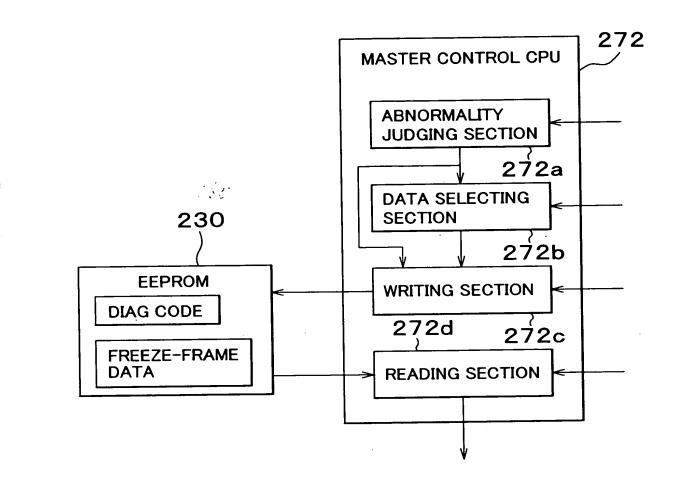
#### F I G. 1



INV: Mitsuhiro NADA SHEET <u>2</u> OF <u>9</u> 220 162 65 67 BRAKE 161 g B 270 63 REGreq 274 REGprac SP AΡ 240 50 CONTROLSECTION POWER SOURCE CONTROL CIRCUIT ¥ ► EE MASTER PEreq WDP 272 ENGINE ECU ENGINE REVen SOC 120 91 212 REV1,REV2,IB WDP F I G. 2 T1req,T2req MAIN ECU 214 EEPROM DRIVING DRIVING CIRCUIT ST WDP a MG1 264 RES 92 280 RES MG2 FIRST MOTOR CONTROL CPU RES Tred MOTOR CONTROL SECTION MOTOR MAIN CONTROL CPU BATTERY 193 266 MDE 94 262 SECOND MOTOR CONTROL CPU | |Zred RES REV2 REV1 <u>≪</u> BATTER 18 230 260

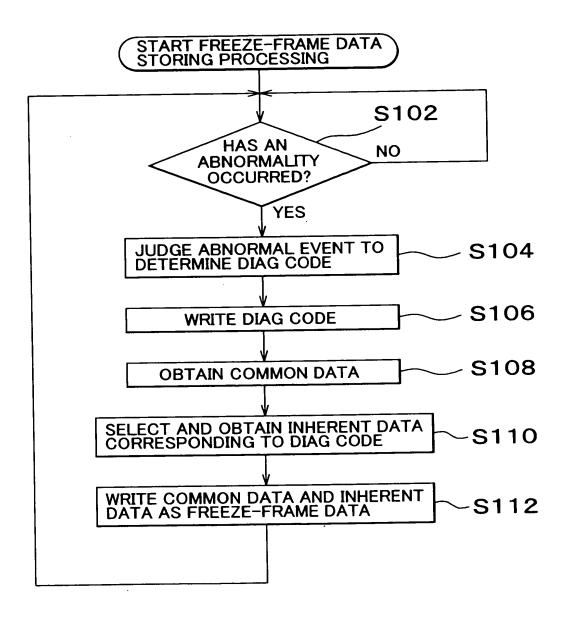
OBLON, SPIVAK, ET AL DOCKET #: 205007US2 OBLON, SPIVAK, ET AL
POCKET #: 205007US2
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FIG. 3



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#### F I G. 4



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F I G. 5B



FIG. 5C

P1300
P1100

FIG. 5D

P1	300
	100
P1	200

FIG. 5E



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#### FIG. 6A

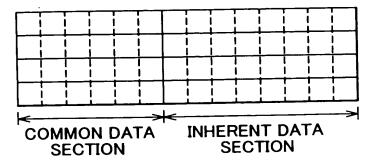


FIG. 6B

#### FRAME1

αt1	β t1	γt1	at1	bt1	ct1	dt1
	!!!			1 1		
	! !					<u> </u>
		1 1	İ	i	<u> </u>	1 1

# FIG. 6C

# FRAME1 FRAME2

αt1	β t1	γt1	at1	bt1	ct1	dt1
αt2	β t2	γt2	et2	ft2	gt2	ht2
				1 1		
				1 1		

# FIG. 6D

FRAME1 FRAME2

FRAME3

	β t1					
αt2	β t2	γt2	et2	ft2	gt2	ht2
α t3	β t3	γt3	it3	jt3	kt3	lt3

# FIG. 6E

FRAME1

FRAME2

FRAME3

FRAME4

					ct1	
αt2	β t2	γt2	et2	ft2	gt2	ht2
					kt3	
αt4	β t4	γt4	mt4	nt4	ot4	pt4

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# FIG. 7A-1

PID=0,FRAME NUMBER=1  $\alpha$ t1  $\beta$ t1  $\gamma$ t1 at1

F I G. 7A-2

PID=1,FRAME NUMBER=1 | bt1

ct1 dt1

et2

F I G. 7A-3

PID=0,FRAME NUMBER=2 αt2 βt2 γt2

F I G. 7B-1

PID=1,FRAME NUMBER=2 ft2 ht2 gt2

F I G. 7B-2

PID=0,FRAME NUMBER=3 |  $\alpha$  t3 |  $\beta$  t3 |  $\gamma$  t3 |

F I G. 7C-1

PID=1,FRAME NUMBER=3 | jt3 lt3 kt3

F I G. 7C-2

. PID=0,FRAME NUMBER=4 |  $\alpha$  t4 |  $\beta$  t4 |  $\gamma$  t4 | mt4

F I G. 7C-3

PID=1,FRAME NUMBER=4 | nt4 | ot4 pt4



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# FIG. 8

FRAME1 FRAME2

FRAME2 FRAME3 FRAME4

αt1	β t1	γt1	at1	bt1	ct1	dt1
αt2	β t2	γ t2	ft2	ht2		
αt3	β t3	γt3	it3	kt3	lt3	
αt4	β t4	γt4	mt4	nt4		

# FIG. 9

FRAME1 FRAME2 FRAME3

FRAME4

αt1	β t1	γt1	at1	bt1	ct1	dt1
αt2	β t2	γt2	et2		ft2	ht2
αt3	β t3	γt3	it3	kt3		lt3
αt4	β t4	γt4	mt4		nt4	pt4

# FIG. 10

FRAME1 FRAME2 FRAME3 FRAME4

αt1	β t1	γt1	at1	bt1	ct1	dt1
αt2	β t2	γt2	ft2	h	t2	
αt3	β t3	γt3	it3	kt3	3	lt3
αt4	β t4	γt4	mt4		nt4	

# F I G. 11

FRAME1 FRAME2 FRAME3 FRAME4

αt1	β t1	γt1	at1	bt1	ct1	dt1
αt2	β t2	γt2	ft2			ht2
α t3	β t3	γt3	it3	kt3		lt3
αt4	β t4	γt4		mt4		nt4

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EXCLUSIVE STORING REGION OF INHERENT DATA etn, ftn, gtn, htn ht2 **gt**2 在2 et2 EXCLUSIVE STORING REGION OF INHERENT DATA atn, btn, ctn, dtn 타 INHERENT DATA SECTION ध <u>p</u> atl EXCLUSIVE STORING REGION OF INHERENT DATA itn, jtn, ktn, ltn <u>ಭ</u> <del>K</del>3 jt3 <u>it3</u> 7 t3 7 22 COMMON DATA SECTION  $\beta$  t2  $\beta$ t3  $\beta$ t1 αt1 αt2 αt3 FRAME1 FRAME2 FRAME3 FRAME4